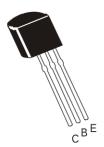


## Continental Device India Limited

An ISO/TS16949 and ISO 9001 Certified Company



## PNP SILICON PLANAR EPITAXIAL TRANSISTORS



BC212, A, B BC213, A, B, C BC214, B, C

TO-92 Plastic Package

# Silicon Small Signal General Purpose Amplifier

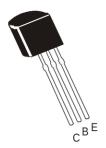
## ABSOLUTE MAXIMUM RATINGS (T<sub>a</sub>=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	BC212	BC213	BC214	UNITS
Collector Emitter Voltage	V <sub>CEO</sub>	50	30	30	V
Collector Base Voltage	$V_{CBO}$	60	45	45	V
Emitter Base Voltage	V <sub>EBO</sub>		5		V
Collector Current Continuous	I <sub>C</sub>		100		mA
Power Dissipation @ T <sub>a</sub> =25°C	P <sub>D</sub>		350		mW
Derate Above 25°C			2.8		mW/ ºC
Power Dissipation @ T <sub>c</sub> =25°C	P <sub>D</sub>		1		W
Derate Above 25°C			8		mW/ ºC
Operating And Storage Junction Temperature Range	$T_{j},T_{stg}$		-55 to +150		°C

#### THERMAL RESISTANCE

Junction to Ambient in free air	R <sub>th (j-a)</sub>	357	°C/W
Junction to case	R <sub>th (j-c)</sub>	125	°C/W

### PNP SILICON PLANAR EPITAXIAL TRANSISTORS



BC212, A, B BC213, A, B, C BC214, B, C

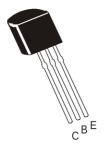
TO-92 Plastic Package

### ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless specified otherwise)

					Ι	
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Emitter Voltage	$V_{CEO}$	I <sub>C</sub> =2mA,I <sub>B</sub> =0				
BC212			50			V
BC213, BC214			30			V
Collector Base Voltage	V <sub>CBO</sub>	I <sub>C</sub> =10uA.I <sub>E</sub> =0				
BC212			60			V
BC213, BC214			45			V
Emitter Base Voltage	V <sub>EBO</sub>	I <sub>E</sub> =10uA, I <sub>C</sub> =0	5			V
Collector Cut off Current	I <sub>CBO</sub>	$V_{CB}=30V,I_{E}=0$			15	nA
Emitter Cut off Current	I <sub>EBO</sub>	$V_{EB}$ =4V, $I_{C}$ =0			15	nA
DC Current Gain						
BC212, BC213	$h_{FE}$	I <sub>C</sub> =10uA,V <sub>CE</sub> =5V	40			
BC214			100			
BC212	h <sub>FE</sub>	I <sub>C</sub> =2mA,V <sub>CE</sub> =5V	60			
BC213			80			
BC214			140		600	
BC212, BC214	h <sub>FE</sub>	I <sub>C</sub> =100mA,V <sub>CE</sub> =5V*		120		
BC213				140		
Collector Emitter Saturation Voltage	V <sub>CE(sat)</sub>	I <sub>C</sub> =10mA,I <sub>B</sub> =0.5mA		0.10		V
	, ,	I <sub>C</sub> =100mA,I <sub>B</sub> =5mA*		0.25	0.6	V
Base Emitter Saturation Voltage	V <sub>BE(sat)</sub>	I <sub>C</sub> =100mA,I <sub>B</sub> =5mA*		1.00	1.4	V
Base Emitter On Voltage	$V_{BE(on)}$	I <sub>C</sub> =2mA,V <sub>CE</sub> =5V	0.6	0.62	0.72	V

<sup>\*</sup>Pulse Condition: Pulse Width =  $300\mu$ s, Duty Cycle = 2%.

### PNP SILICON PLANAR EPITAXIAL TRANSISTORS



BC212, A, B BC213, A, B, C BC214, B, C

TO-92 Plastic Package

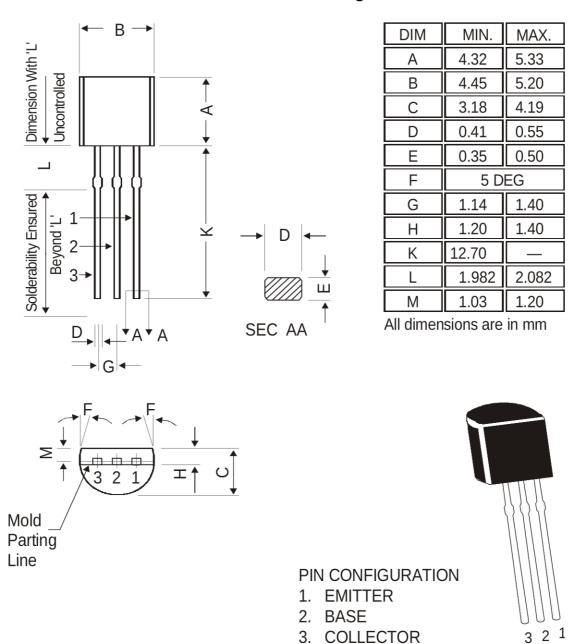
### ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C unless specified otherwise)

#### **DYNAMICS CHARACTERISTICS**

DESCRIPTION OF THE PROPERTY OF	CVMDOL	TEST COMPLETION	MAINI	TVD	MAY	LINUTO
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Transition Frequency						
BC212	f <sub>T</sub>	I <sub>C</sub> =10mA, V <sub>CE</sub> =5V		280		MHz
BC213		f=50MHz		360		MHz
BC214				320		MHz
Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> =10V, I <sub>E</sub> =0			6	pF
Noise Figure						
BC212, BC213	NF	I <sub>C</sub> =200uA, V <sub>CE</sub> =5V			10	dB
		R <sub>S</sub> =2KΩ f=1KHz				
		f=200H <sub>Z</sub>				
BC214	NF	I <sub>C</sub> =200uA, V <sub>CE</sub> =5V			2	dB
		R <sub>S</sub> =2KΩ f=30Hz				
		to 15KHz				
Small Signal Current Gain						
BC212	h <sub>fe</sub>	I <sub>C</sub> =2mA, V <sub>CE</sub> =5V	60			
BC213		f=1KH <sub>Z</sub>	80			
BC214			140			
BC212A, BC213A	h <sub>fe</sub>	I <sub>C</sub> =2mA, V <sub>CE</sub> =5V	100		300	
BC212B, BC213B, BC214B		f=1KH <sub>Z</sub>	200		400	
BC213C, BC214C			350		600	

<sup>\*</sup>Pulse Condition: Pulse Width =  $300\mu$ s, Duty Cycle = 2%.

#### **TO-92 Plastic Package**



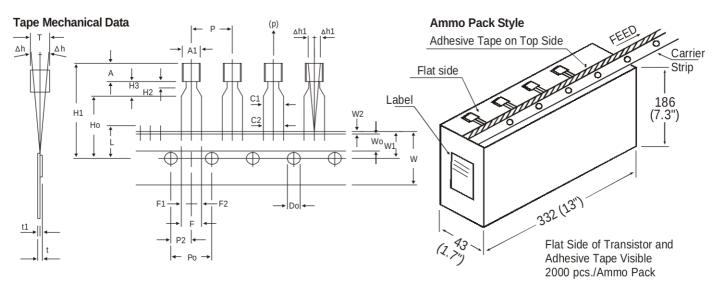
The TO-92 Package, Tape and Ammo Pack drawings are correct as on the date of issue/revision of this Data Sheet.

The currently valid dimensions and information, may please be confirmed from the TO-92 Drawing in the Packages and Packing Section of the Product Catalogue.

#### **Packing Details**

PACKAGE	STANDARDPACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net: Weight/Qty	Size	Qty	Size	Qty	GrWt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

#### **TO-92 Tape and Ammo Pack**



#### All dimensions are in mm

		SPECIFICATION				
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.	
BODY WIDTH	A1	4.0		4.8		NOTES
BODY HEIGHT	A	4.8		5.2		1. Maximum
BODY THICKNESS	Т	3.9		4.2		leads will
PITCH OF COMPONENT	P		12.7		± 1.0	2. Maximum
*1FEED HOLE PITCH	Po		12.7		± 0.3	between
*2 FEED HOLE CENTRE TO COMPONENT CENTRE						exceed 1
	P2		6.35		± 0.4	3. Holddowr
DISTANCE BETWEEN OUTER LEADS	F		5.08		+ 0.6 - 0.2	the edge( shall be n
*3 COMPONENT ALIGNMENT SIDE VIEW	∆h		0	1.0		4. There will
*4 COMPONENT ALIGNMENT FRONT VIEW	△h1		0	1.3		consecuti
TAPE WIDTH	W		18		± 0.5	tape.
HOLD-DOWN TAPE WIDTH	Wo		6		± 0.2	5. A tape tra
HOLE POSITION	W1		9		+ 0.7 - 0.5	compone
HOLD-DOWN TAPE POSITION	W2		0.5		± 0.2	6. Splices sl
LEAD WIRE CLINCH HEIGHT	Но		16		± 0.5	sprocket 1
COMPONENT HEIGHT	H1			23.25		
LENGTH OF SNIPPED LEADS	L			11.0		
FEED HOLE DIAMETER	Do		4		± 0.2	REMARKS
*5 TOTAL TAPE THICKNESS	t			1.2		
LEAD - TO - LEAD DISTANCE	F1, F2		2.54		+ 0.4	*1 Cumulati
STAND OFF	H2	0.45		1.45	- 0.1	*2 To be me
CLINCH HEIGHT	H3			3.0		*3 At top of
LEAD PARALLELISM	C1 - C2			0.22		*4 At top of
PULL - OUT FORCE	(p)	6N				*5 t1 0.3 -

- n alignment deviation between II not to be greater than 0.2mm.
- m non-cumulative variation tape feed holes shall not 1 mm in 20 pitches.
- n tape will not exceed beyond e(s) of carrier tape and there no exposure of adhesive.
- ill be no more than three (3) tive missing components in a
- ailer, having at least three feed e provided after the last ent in a tape.
- should not interfere with the feed holes.
- tive pitch error 1.0 mm/20 pitch
- easured at bottom of clinch
- body
- body
- 0.6 mm

**Notes** 

BC212, A, B BC213, A, B, C BC214, B, C

TO-92 Plastic Package

#### **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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