

Thyristor \ Diode Module

$$V_{RRM} = 2 \times 1600 \text{ V}$$

$$I_{TAV} = 27 \text{ A}$$

$$V_T = 1.27 \text{ V}$$

Phase leg

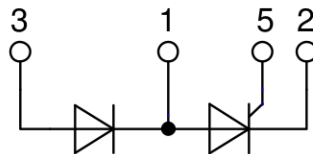
Part number

MCD26-16io8B



Backside: isolated

 E72873



Features / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long-term stability
- Direct Copper Bonded Al₂O₃-ceramic

Applications:

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

Package: TO-240AA

- Isolation Voltage: 3600 V~
- Industry standard outline
- RoHS compliant
- Soldering pins for PCB mounting
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

Terms Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact your local sales office.

Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact your local sales office.

Should you intend to use the product in aviation, in health or life endangering or life support applications, please notify. For any such application we urgently recommend

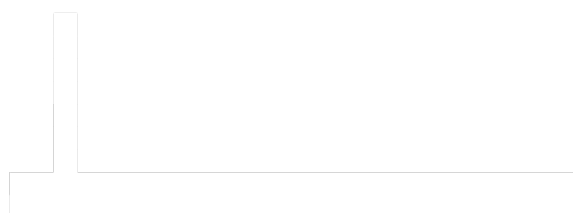
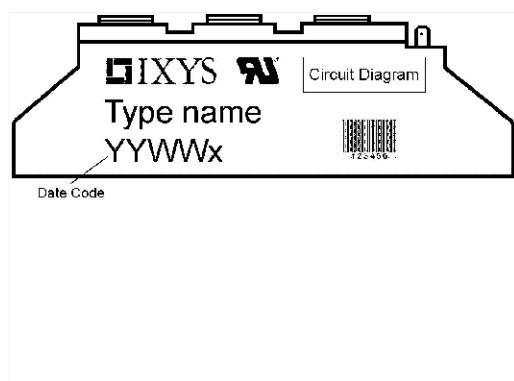
- to perform joint risk and quality assessments;

- the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

Rectifier				Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V _{RSM/DSM}	max. non-repetitive reverse/forward blocking voltage	T _{VJ} = 25°C				1700	V
V _{RRM/DRM}	max. repetitive reverse/forward blocking voltage	T _{VJ} = 25°C				1600	V
I _{R/D}	reverse current, drain current	V _{R/D} = 1600 V	T _{VJ} = 25°C			100	μA
		V _{R/D} = 1600 V	T _{VJ} = 125°C			3	mA
V _T	forward voltage drop	I _T = 40 A	T _{VJ} = 25°C			1.27	V
		I _T = 80 A				1.64	V
		I _T = 40 A	T _{VJ} = 125°C			1.27	V
		I _T = 80 A				1.65	V
I _{TAV}	average forward current	T _C = 85°C	T _{VJ} = 125°C			27	A
I _{T(RMS)}	RMS forward current	180° sine				42	A
V _{T0}	threshold voltage	for power loss calculation only		T _{VJ} = 125°C		0.85	V
r _T	slope resistance					11	mΩ
R _{thJC}	thermal resistance junction to case					0.88	K/W
R _{thCH}	thermal resistance case to heatsink				0.20		K/W
P _{tot}	total power dissipation	T _C = 25°C				115	W
I _{TSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	T _{VJ} = 45°C			520	A
		t = 8,3 ms; (60 Hz), sine	V _R = 0 V			560	A
		t = 10 ms; (50 Hz), sine	T _{VJ} = 125°C			440	A
		t = 8,3 ms; (60 Hz), sine	V _R = 0 V			475	A
I ² t	value for fusing	t = 10 ms; (50 Hz), sine	T _{VJ} = 45°C			1.35	kA²s
		t = 8,3 ms; (60 Hz), sine	V _R = 0 V			1.31	kA²s
		t = 10 ms; (50 Hz), sine	T _{VJ} = 125°C			970	A²s
		t = 8,3 ms; (60 Hz), sine	V _R = 0 V			940	A²s
C _J	junction capacitance	V _R = 400 V f = 1 MHz	T _{VJ} = 25°C		22		pF
P _{GM}	max. gate power dissipation	t _p = 30 μs	T _C = 125°C			10	W
		t _p = 300 μs				5	W
P _{GAV}	average gate power dissipation					0.5	W
(di/dt) _{cr}	critical rate of rise of current	T _{VJ} = 125°C; f = 50 Hz repetitive, I _T = 45 A				150	A/μs
		t _p = 200 μs; di _G /dt = 0.45 A/μs; I _G = 0.45 A; V = ⅔ V _{DRM} non-repet., I _T = 27 A				500	A/μs
(dv/dt) _{cr}	critical rate of rise of voltage	V = ⅔ V _{DRM} T _{VJ} = 125°C				1000	V/μs
		R _{GK} = ∞; method 1 (linear voltage rise)					
V _{GT}	gate trigger voltage	V _D = 6 V T _{VJ} = 25°C				1.5	V
			T _{VJ} = -40°C			1.6	V
I _{GT}	gate trigger current	V _D = 6 V T _{VJ} = 25°C				100	mA
			T _{VJ} = -40°C			200	mA
V _{GD}	gate non-trigger voltage	V _D = ⅔ V _{DRM} T _{VJ} = 125°C				0.2	V
I _{GD}	gate non-trigger current					10	mA
I _L	latching current	t _p = 10 μs T _{VJ} = 25°C				450	mA
		I _G = 0.45 A; di _G /dt = 0.45 A/μs					
I _H	holding current	V _D = 6 V R _{GK} = ∞ T _{VJ} = 25°C				200	mA
t _{gd}	gate controlled delay time	V _D = ½ V _{DRM} T _{VJ} = 25°C				2	μs
		I _G = 0.45 A; di _G /dt = 0.45 A/μs					
t _q	turn-off time	V _R = 100 V; I _T = 20 A; V = ⅔ V _{DRM} T _{VJ} = 100 °C di/dt = 10 A/μs dv/dt = 20 V/μs t _p = 200 μs		150			μs

Package TO-240AA				Ratings			
Symbol	Definition	Conditions		min.	typ.	max.	Unit
I _{RMS}	RMS current	per terminal				200	A
T _{VJ}	virtual junction temperature			-40		125	°C
T _{op}	operation temperature			-40		100	°C
T _{stg}	storage temperature			-40		125	°C
Weight					81		g
M _D	mounting torque			2.5		4	Nm
M _T	terminal torque			2.5		4	Nm
d _{Spp/App}	creepage distance on surface striking distance through air	terminal to terminal	13.0	9.7			mm
d _{Spb/Apb}		terminal to backside	16.0	16.0			mm
V _{ISOL}	isolation voltage	t = 1 second	50/60 Hz, RMS; I _{ISOL} ≤ 1 mA		3600		V
		t = 1 minute			3000		V

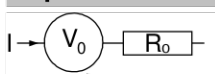


Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MCD26-16io8B	MCD26-16io8B	Box	36	453277

Similar Part	Package	Voltage class
MCMA35PD1600TB	TO-240AA-1B	1600
MCMA50PD1600TB	TO-240AA-1B	1600

Equivalent Circuits for Simulation

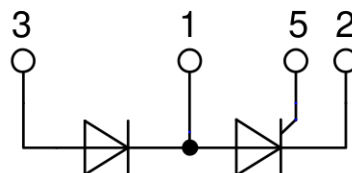
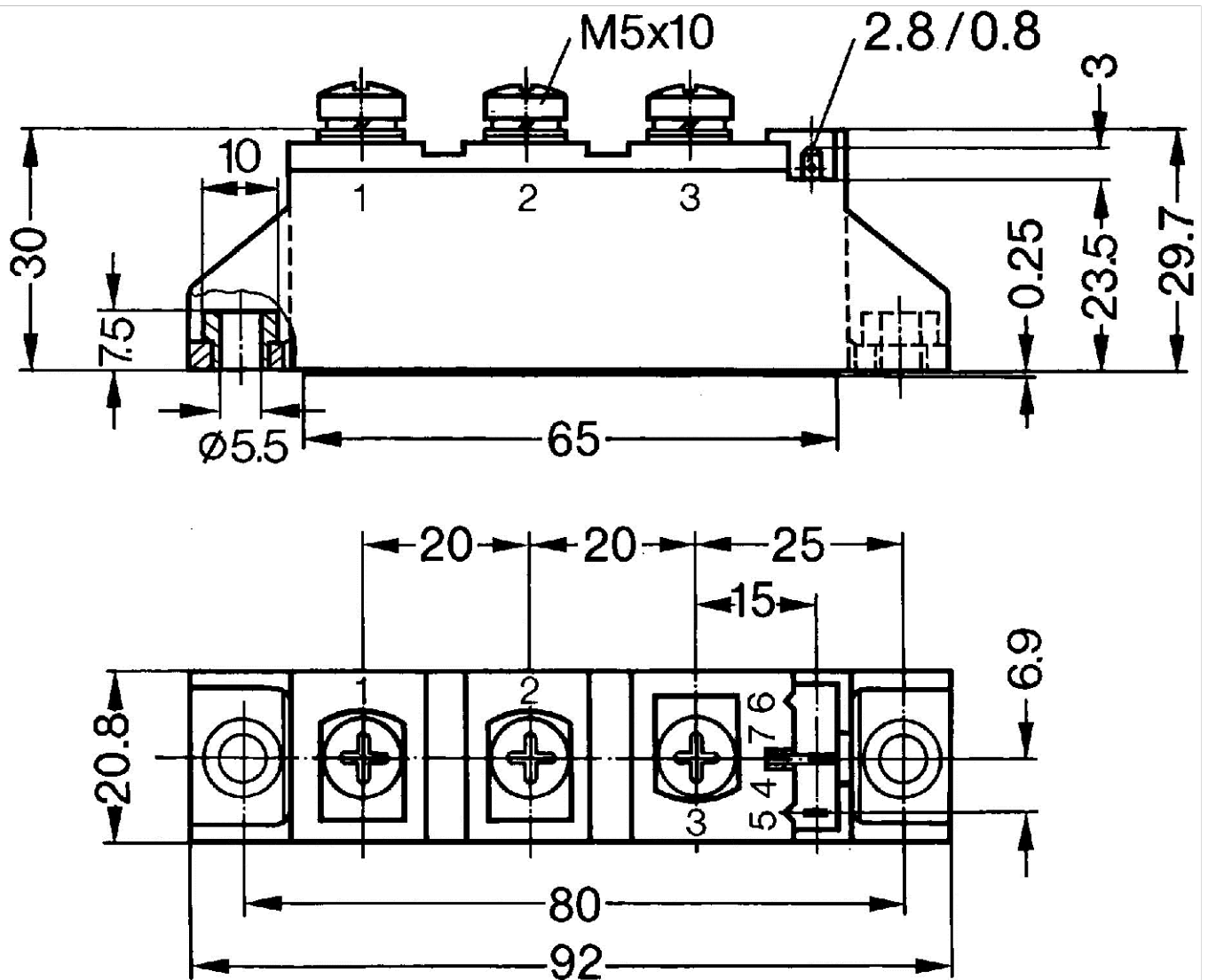
* on die level

 $T_{VJ} = 125^\circ\text{C}$ 

Thyristor

$V_{0\max}$	threshold voltage	0.85	V
$R_{0\max}$	slope resistance *	9.8	mΩ

Outlines TO-240AA



Thyristor

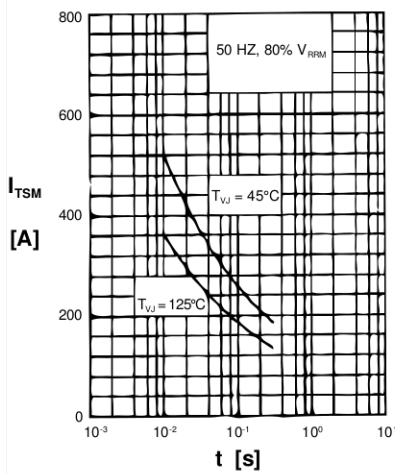


Fig. 1 Surge overload current
 I_{TSM} : Crest value, t : duration

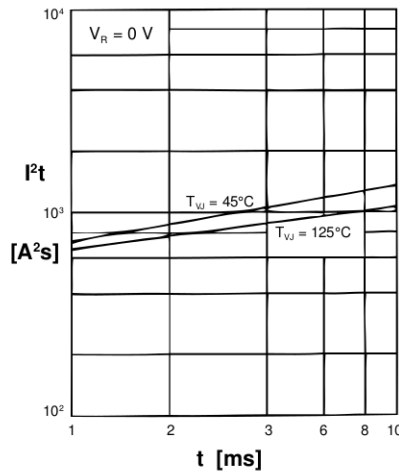


Fig. 2 I^2t versus time (1-10 ms)

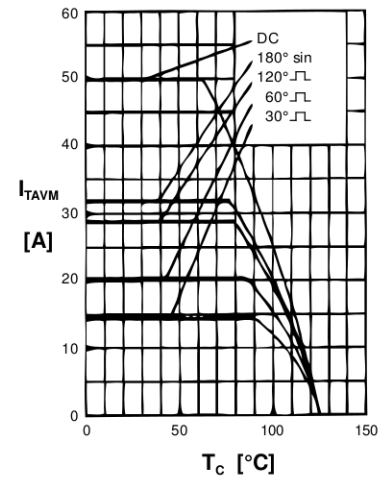


Fig. 3 Max. forward current
at case temperature

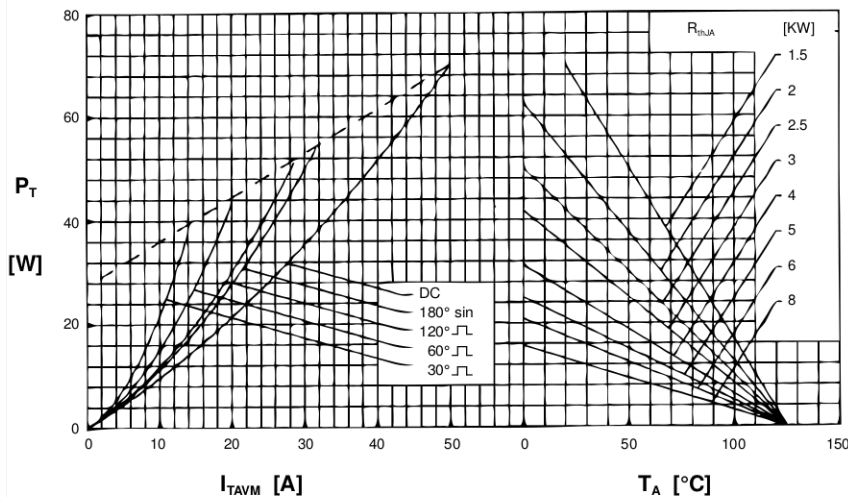


Fig. 4 Power dissipation versus onstate current & ambient temp. (per thyristor)

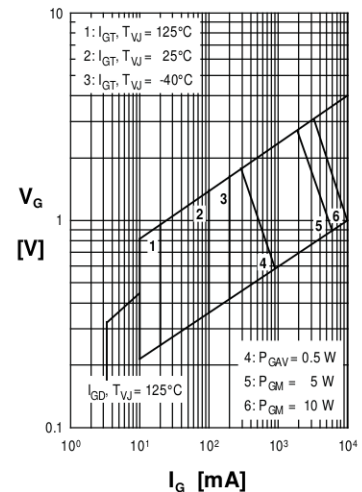


Fig. 5 Gate trigger charact.

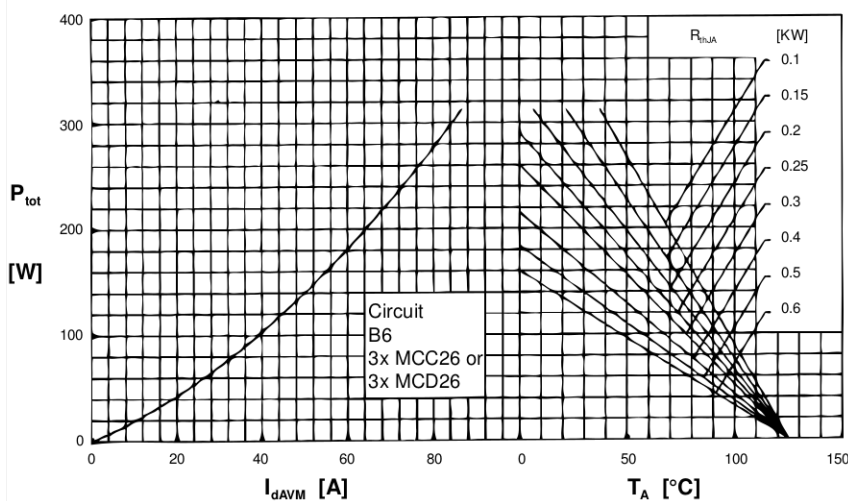


Fig. 6 Three phase rectifier bridge: Power dissipation versus direct
output current and ambient temperature

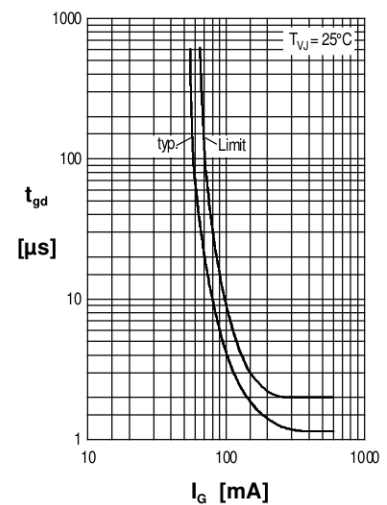


Fig. 7 Gate trigger delay time

Rectifier

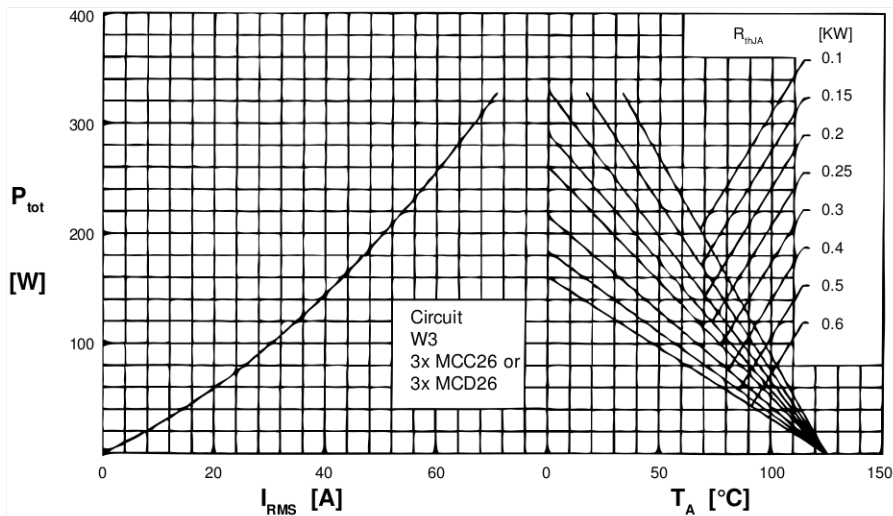


Fig. 8 Three phase AC-controller: Power dissipation vs. RMS output current and ambient temperature

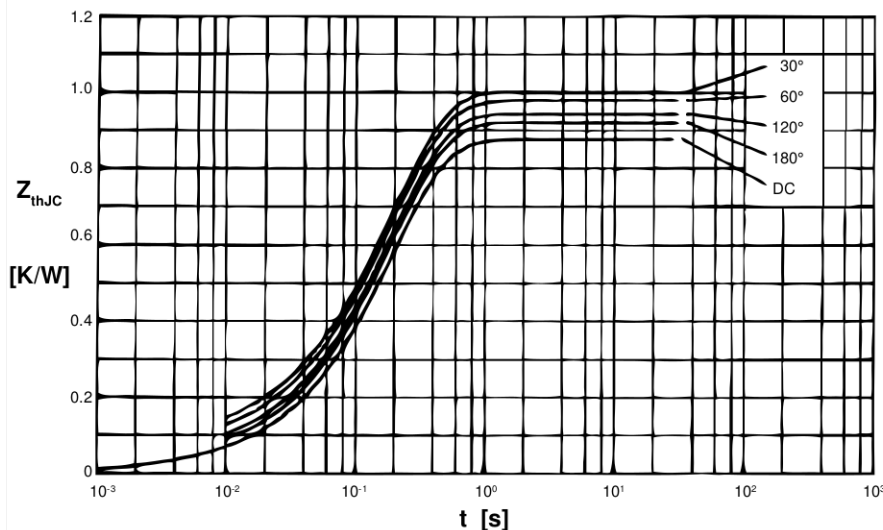


Fig. 9 Transient thermal impedance junction to case (per thyristor)

R_{thJC} for various conduction angles d:

d	R_{thJC} [K/W]
DC	0.88
180°	0.92
120°	0.95
60°	0.98
30°	1.01

Constants for Z_{thJC} calculation:

i	R_{thi} [K/W]	t_i [s]
1	0.019	0.0031
2	0.029	0.0216
3	0.832	0.1910

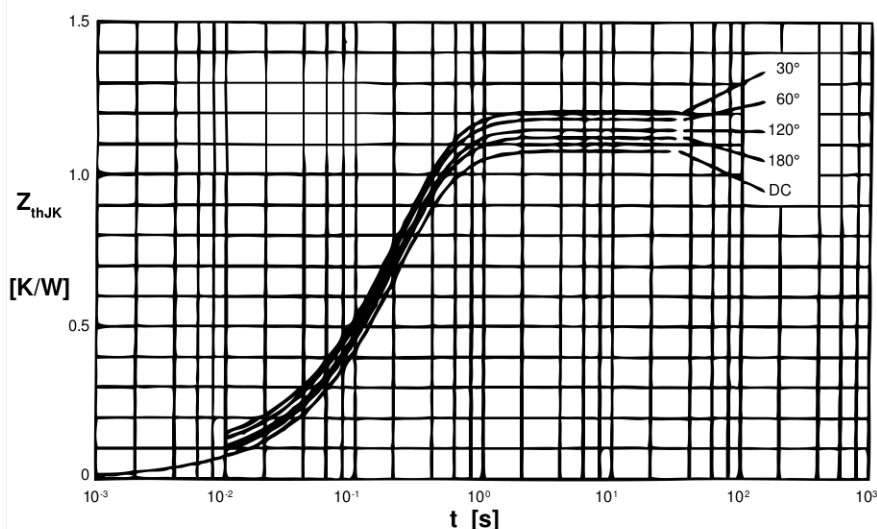


Fig. 10 Transient thermal impedance junction to heatsink (per thyristor)

R_{thJK} for various conduction angles d:

d	R_{thJK} [K/W]
DC	1.08
180°	1.12
120°	1.15
60°	1.18
30°	1.21

Constants for Z_{thJK} calculation:

i	R_{thi} [K/W]	t_i [s]
1	0.019	0.0031
2	0.029	0.0216
3	0.832	0.1910
4	0.200	0.4500