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NPN Epitaxial Silicon Transistor

September 2011

Features

- · TV Vertical Deflection Output
- Complement to KSA940
- Collector-Base Voltage : V_{CBO} = 150V



1.Base 2.Collector 3.Emitter

Absolute Maximum Ratings $T_A = 25$ °C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	150	V
V _{CEO}	Collector-Emitter Voltage	150	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current	1.5	Α
P _C	Collector Dissipation (T _C = 25°C)	25	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 to 150	°C

Electrical Characteristics $T_A = 25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = 500 \mu A, I_E = 0$	150			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = 10mA, I _B = 0	150			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 500 \mu A, I_C = 0$	5			V
I _{CBO}	Collector Cut-off Current	V _{CB} = 120V, I _E = 0			10	μА
h _{FE}	DC Current Gain	V _{CE} = 10V, I _C = 0.5A	40	75	140	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 500mA, I _B = 50mA			1	V
f _T	Current Gain Bandwidth Product	V _{CE} = 10V, I _C = 0.5A		4		MHz
C _{ob}	Output Capacitance	$V_{CB} = 10V, I_{E} = 0,$ f = 1MHz		50		pF

h_{FE} Classification

Classification	H1	H2
h _{FE}	40 ~ 80	60 ~ 125

Typical Performance Characteristics

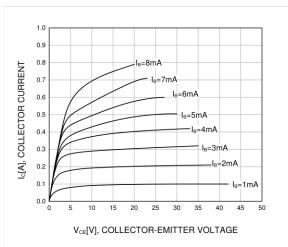
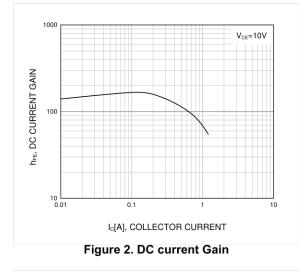


Figure 1. Static Characteristic



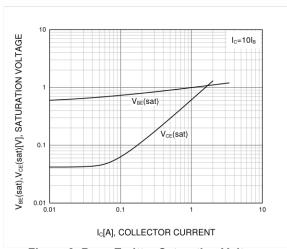


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

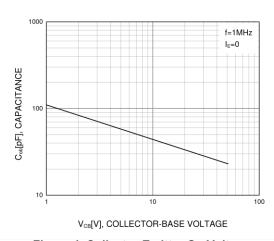


Figure 4. Collector-Emitter On Voltage

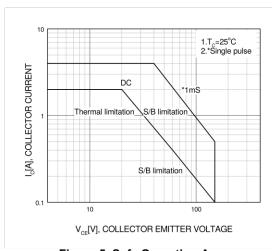


Figure 5. Safe Operating Area

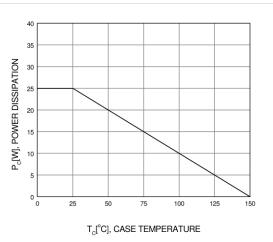
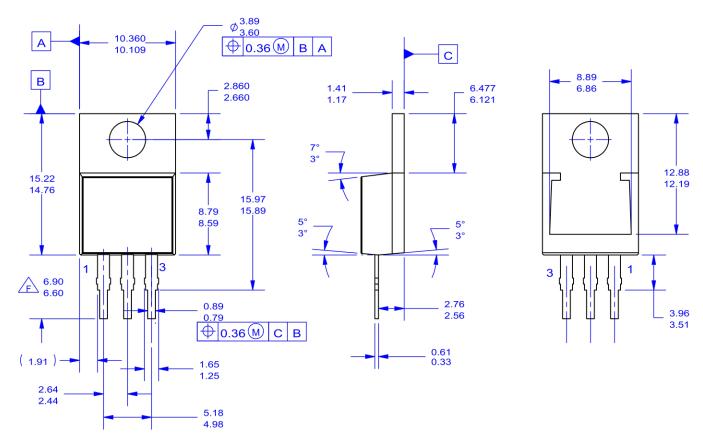
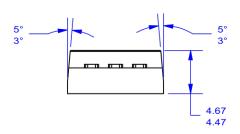


Figure 6. Power Derating



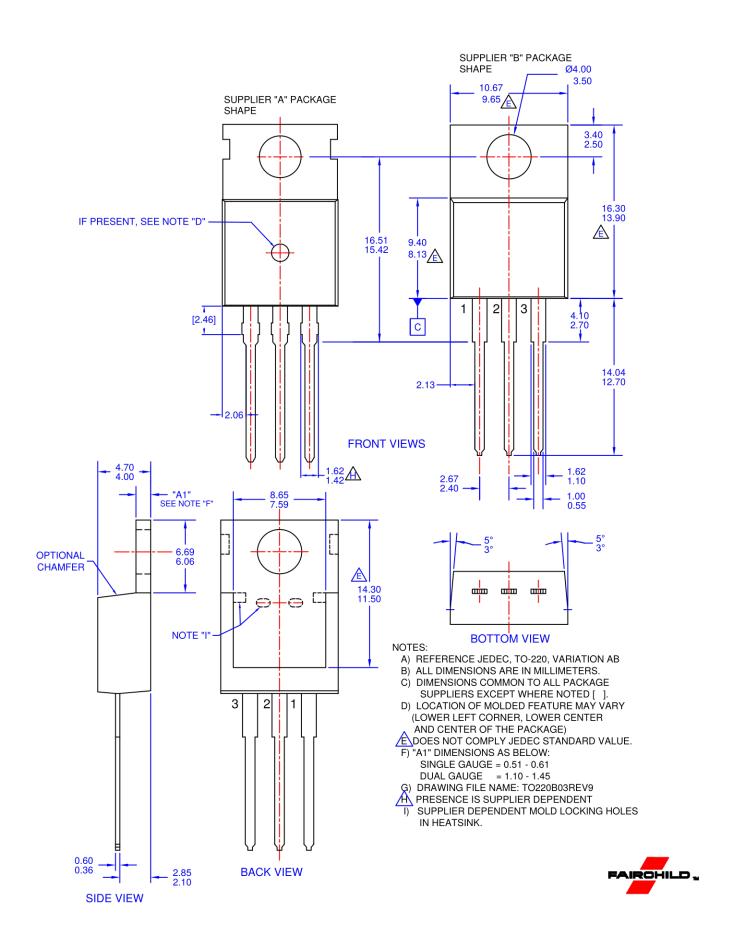


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