

# TN1625 TYN616, TYN816

#### 16 A standard SCRs

#### **Features**

- I<sub>T(RMS)</sub> =16 A
- $V_{DRM}/V_{RRM} = 600 \text{ to } 1000 \text{ V}$
- I<sub>GT</sub> = 25 mA

### **Description**

The standard TN16 / TYNx16 16 A SCRs series is suitable for general purpose applications.

Using clip assembly technology, they provide a superior performance in surge current capabilities.

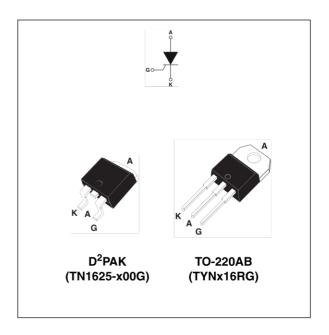


Table 1. Device summary

Parameter	TN1625-600G TYN616RG	TYN816RG	TN1625-1000G	Unit
$V_{DRM}/V_{RRM}$	600	800	1000	V
Sensitivity	25	25	25	mA

## 1 Characteristics

Table 2. Absolute ratings (limiting values)

Symbol	Parameter	Value	Unit			
I <sub>T(RMS)</sub>	RMS on-state current (180 °Conduction angle	16	Α			
I <sub>T(AV)</sub>	Average on-state current (180 °Conduction angle) T <sub>c</sub> = 110 °C			10	Α	
	Non repetitive curse peak on state current	$t_p = 8.3 \text{ ms}$	T - 25 °C	200	Α	
ITSM	Non repetitive surge peak on-state current	$t_p = 10 \text{ ms}$	− T <sub>j</sub> = 25 °C	190		
l <sup>2</sup> t	$I^2$ t Value for fusing $t_p = 10 \text{ ms}$		T <sub>j</sub> = 25 °C	180	A <sup>2</sup> s	
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$ , $t_r \le 100 \text{ ns}$ $F = 60 \text{ Hz}$		T <sub>j</sub> = 125 °C	50	A/µs	
I <sub>GM</sub>	Peak gate current $t_p = 20 \mu s$ $T_j = 125  ^{\circ}C$		T <sub>j</sub> = 125 °C	4	Α	
P <sub>G(AV)</sub>	Average gate power dissipation	1	W			
T <sub>stg</sub> T <sub>j</sub>	Storage junction temperature range Operating junction temperature range			- 40 to + 150 - 40 to + 125	°C	
V <sub>RGM</sub>	Maximum peak reverse gate voltage			5	V	

Table 3. Electrical characteristics ( $T_j = 25$  °C, unless otherwise specified)

Symbol	Test Conditions			Value	Unit
la-			MIN.	2	mA
I <sub>GT</sub>	$V_D = 12 V$ $R_L = 33 \Omega$		MAX.	25	
V <sub>GT</sub>		MAX.	1.3	V	
V <sub>GD</sub>	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$	T <sub>j</sub> = 125 °C	MIN.	0.2	V
I <sub>H</sub>	I <sub>T</sub> = 500 mA Gate open		MAX.	40	mA
ΙL	I <sub>G</sub> = 1.2 x I <sub>GT</sub>		MAX.	60	mA
dV/dt	$V_D = 67 \% V_{DRM}$ Gate open $T_j = 12$		MIN.	500	V/µs
V <sub>TM</sub>	$I_{TM} = 32 \text{ A}$ $t_p = 380  \mu\text{s}$ $T_j =$		MAX.	1.6	V
V <sub>t0</sub>	Threshold voltage	T <sub>j</sub> = 125 °C	MAX.	0.77	V
R <sub>d</sub>	Dynamic resistance $T_j = 125$ °C		MAX.	23	mΩ
I <sub>DRM</sub>	V <sub>DRM</sub> = V <sub>RRM</sub>	T <sub>j</sub> = 25 °C	MAX.	5	μΑ
I <sub>RRM</sub>	VDRM - VRRM	IVIAX.	2	mA	

Table 4. Thermal resistance

Symbol	Parameter			Value	Unit
R <sub>th(j-c)</sub>	Junction to case (DC)			1.1	°C/W
В	lunction to ambient (DC)	$S = 01 \text{ cm}^2$	D <sup>2</sup> PAK	45	°C/W
$R_{th(j-a)}$	Junction to ambient (DC)		TO-220AB	60	C/VV

S = copper surface under tab

Figure 1. Maximum average power dissipation versus average on-state current

Figure 2. Average and D.C. on-state current versus case temperature

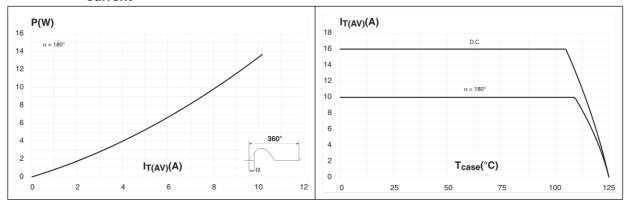


Figure 3. Average and D.C. on-state current versus ambient temperature (copper surface under tab: S=1cm²) (D²PAK)

Figure 4. Relative variation of thermal impedance versus pulse duration

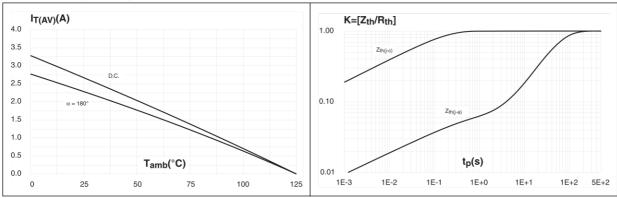


Figure 5. Relative variation of gate trigger current, holding current and latching current versus junction temperature

Figure 6. Surge peak on-state current versus number of cycles

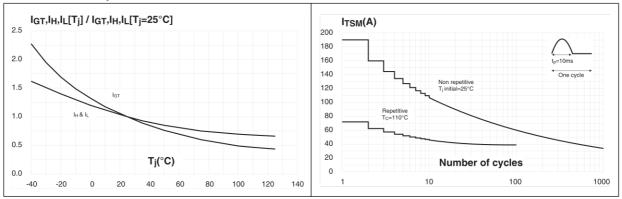


Figure 7. Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 10$  ms, and corresponding values of  $I^2t$ 

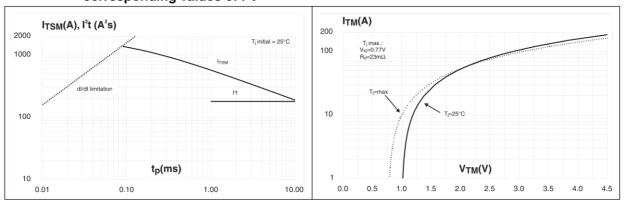
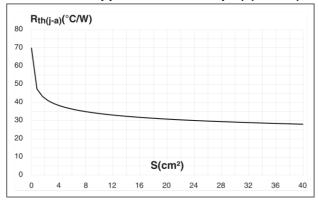


Figure 9. Thermal resistance junction to ambient versus copper surface under tab (epoxy printed circuit board FR4, copper thickness: 35 µm) (D<sup>2</sup>PAK)



## 2 Ordering information scheme

Figure 10. TN1625

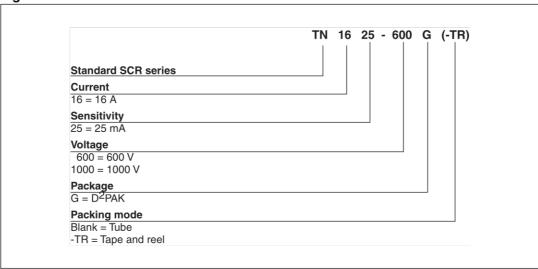
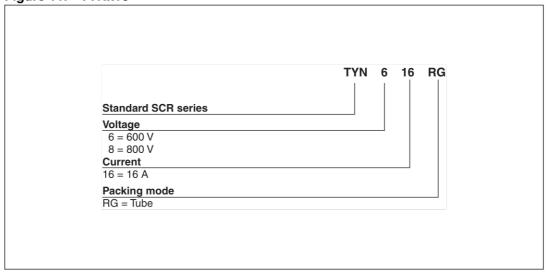


Figure 11. TYNx16



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### 3 Package information

- Epoxy meets UL94,V0
- Cooling method: C
- Recommended torque value: 0.4 0.6 N⋅m

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

Table 5. TO-220AB dimensions

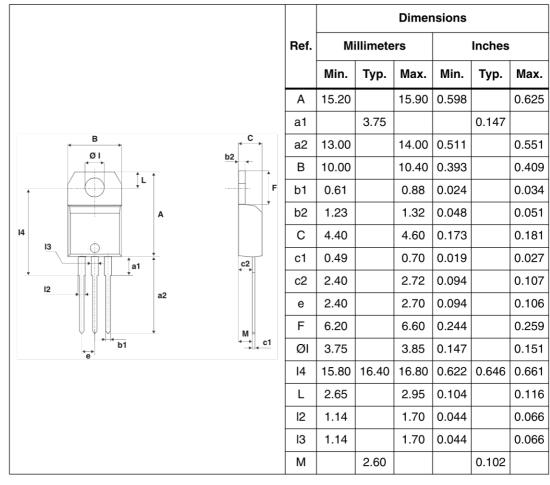
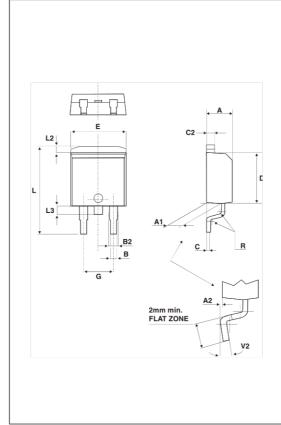
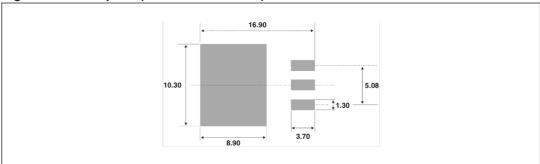


Table 6. D<sup>2</sup>PAK dimensions



	Dimensions					
Ref.	Millimeter		ers			
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	4.30		4.60	0.169		0.181
A1	2.49		2.69	0.098		0.106
A2	0.03		0.23	0.001		0.009
В	0.70		0.93	0.027		0.037
B2	1.25	1.40		0.048	0.055	
С	0.45		0.60	0.017		0.024
C2	1.21		1.36	0.047		0.054
D	8.95		9.35	0.352		0.368
Е	10.00		10.28	0.393		0.405
G	4.88		5.28	0.192		0.208
L	15.00		15.85	0.590		0.624
L2	1.27		1.40	0.050		0.055
L3	1.40		1.75	0.055		0.069
R		0.40			0.016	
V2	0°		8°	0°		8°

Figure 12. Footprint (dimensions in mm)



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## 4 Ordering information

Table 7. Ordering information

Order code <sup>(1)</sup>	Marking <sup>(1)</sup>	Package	Weight	Base qty	Delivery mode
TN1625-x00G	TN1625x00G	D <sup>2</sup> PAK	1.5 g	50	Tube
TN1625-x00G-TR	TN1625x00G	D <sup>2</sup> PAK	1.5 g	1000	Tape and reel
TYNx16RG	TYNx16	TO-220AB	2.3 g	50	Tube

<sup>1.</sup> x indicates votage, 6, 8 or 10 for 600, 800 and 1000 V respectively

# 5 Revision history

Table 8. Document revision history

Date	Revision	Changes
Apr-2002	4A	Last update.
13-Feb-2006	5	TO-220AB delivery mode changed from bulk to tube. ECOPACK statement added.
07-Nov-2007	6	Reformatted to current standards. <i>Table 2</i> .: I <sub>T(RMS)</sub> value corrected from 12 A to 16 A

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