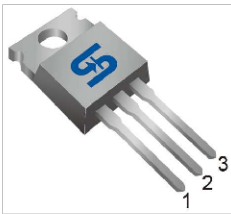
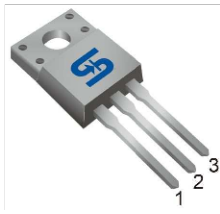


TO-220



ITO-220



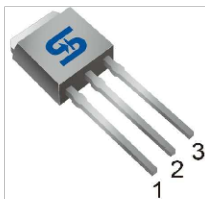
Pin Definition:

1. Gate
2. Drain
3. Source

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)
900	5.1 @ V _{GS} =10V	1.25

TO-251 (IPAK)



TO-252 (DPAK)



General Description

The TSM3N90 N-Channel Power MOSFET is produced by new advance planar process. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode.

Features

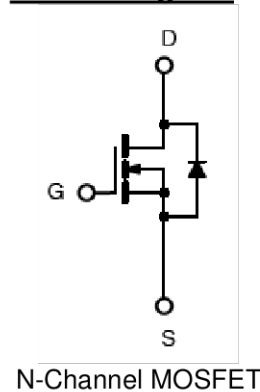
- Low R_{DS(ON)} 4.3Ω (Typ.)
- Low gate charge typical @ 17nC (Typ.)
- Low Crss typical @ 8.7pF (Typ.)

Ordering Information

Part No.	Package	Packing
TSM3N90CH C5G	TO-251	75pcs / Tube
TSM3N90CP ROG	TO-252	2.5Kpcs / 13" Reel
TSM3N90CZ C0G	TO-220	50pcs / Tube
TSM3N90CI C0G	ITO-220	50pcs / Tube

Note: "G" denotes for Halogen Free

Block Diagram



Absolute Maximum Rating (T_a = 25°C unless otherwise noted)

Parameter	Symbol	Limit			Unit
		IPAK/DPAK	ITO-220	TO-220	
Drain-Source Voltage	V _{DS}	900			V
Gate-Source Voltage	V _{GS}	±30			V
Continuous Drain Current	I _D	2.5			A
		1.6			A
Pulsed Drain Current *	I _{DM}	10			A
Single Pulse Avalanche Energy (Note 2)	E _{AS}	10			mJ
Avalanche Current (Repetitive) (Note 1)	I _{AR}	2.5			A
Repetitive Avalanche Energy (Note 1)	E _{AR}	9.4			mJ
Peak Diode Recovery dv/dt (Note 3)	dv/dt	4.5			V/ns
Total Power Dissipation @ T _C = 25°C	P _{TOT}	94	32	94	W
Operating Junction Temperature	T _J	150			°C
Storage Temperature Range	T _{STG}	-55 to +150			°C

Note: Limited by maximum junction temperature

Thermal Performance

Parameter	Symbol	I _{PAK} /D _{PAK}	I _{TO-220}	T _{O-220}	Unit
Thermal Resistance - Junction to Case	$R_{\theta_{JC}}$	1.33	1.33	3.9	°C/W
Thermal Resistance - Junction to Ambient	$R_{\theta_{JA}}$	110	62.5		

Electrical Specifications (T_a = 25°C unless otherwise noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	BV _{DSS}	900	--	--	V
Drain-Source On-State Resistance	V _{GS} = 10V, I _D = 1.25A	R _{DS(ON)}	--	4.3	5.1	Ω
Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	V _{GS(TH)}	2.0	--	4.0	V
Zero Gate Voltage Drain Current	V _{DS} = 900V, V _{GS} = 0V	I _{DSS}	--	--	10	μA
Gate Body Leakage	V _{GS} = ±30V, V _{DS} = 0V	I _{GSS}	--	--	±100	nA
Forward Transfer Conductance	V _{DS} = 30V, I _D = 1.25A	g _{fs}	--	3	--	S
Dynamic						
Total Gate Charge	V _{DS} = 720V, I _D = 2.5A, V _{GS} = 10V	Q _g	--	17	--	nC
Gate-Source Charge		Q _{gs}	--	2.4	--	
Gate-Drain Charge		Q _{gd}	--	6.6	--	
Input Capacitance	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	C _{iss}	--	748	--	pF
Output Capacitance		C _{oss}	--	55	--	
Reverse Transfer Capacitance		C _{rss}	--	8.7	--	
Switching						
Turn-On Delay Time	V _{GS} = 10V, I _D = 2.5A, V _{DD} = 450V, R _G = 25Ω	t _{d(on)}	--	16	--	nS
Turn-On Rise Time		t _r	--	25	--	
Turn-Off Delay Time		t _{d(off)}	--	63	--	
Turn-Off Fall Time		t _f	--	31	--	
Source-Drain Diode Ratings and Characteristic						
Source Current	Integral reverse diode in the MOSFET	I _S	--	--	2.5	A
Source Current (Pulse)		I _{SM}	--	--	10	A
Diode Forward Voltage	I _S = 2.5A, V _{GS} = 0V	V _{SD}	--	--	1.5	V
Reverse Recovery Time	V _{GS} = 0V, I _S = 2.5A,	t _{fr}	--	355	--	nS
Reverse Recovery Charge	dI _F /dt = 100A/μs	Q _{fr}	--	1.8	--	μC

Note 1: Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

Note 2: Max Rating E_{AS} Test Condition: V_{DD} = 50V, I_{AS} = 2A, L = 5mH, R_G = 25Ω, Starting T_J = 25°C

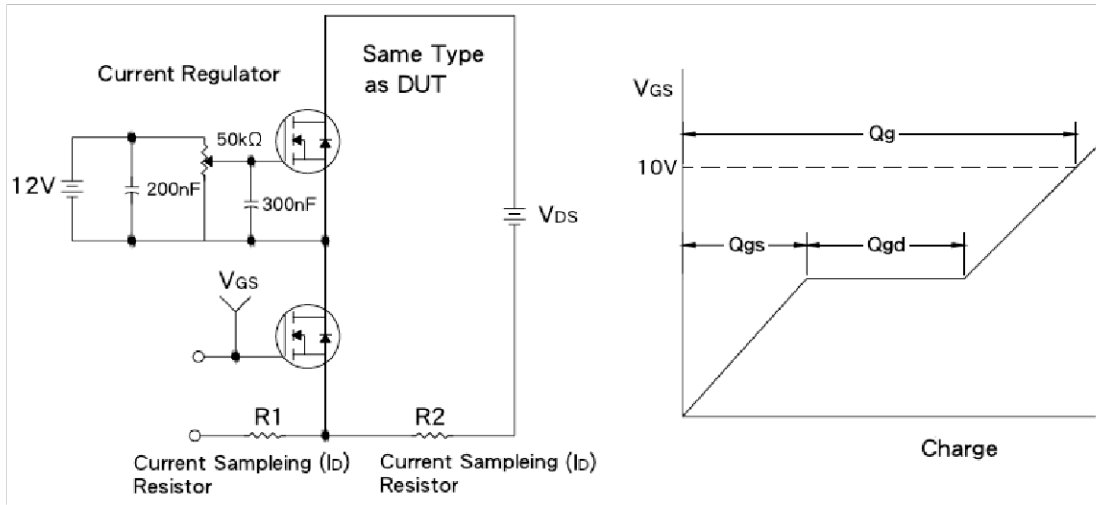
Guaranteed 100% E_{AS} Test Condition: V_{DD} = 50V, I_{AS} = 2A, L = 1mH, R_G = 25Ω, Starting T_J = 25°C

Note 3: I_{SD} ≤ 2.5A, di/dt ≤ 200A/μS, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

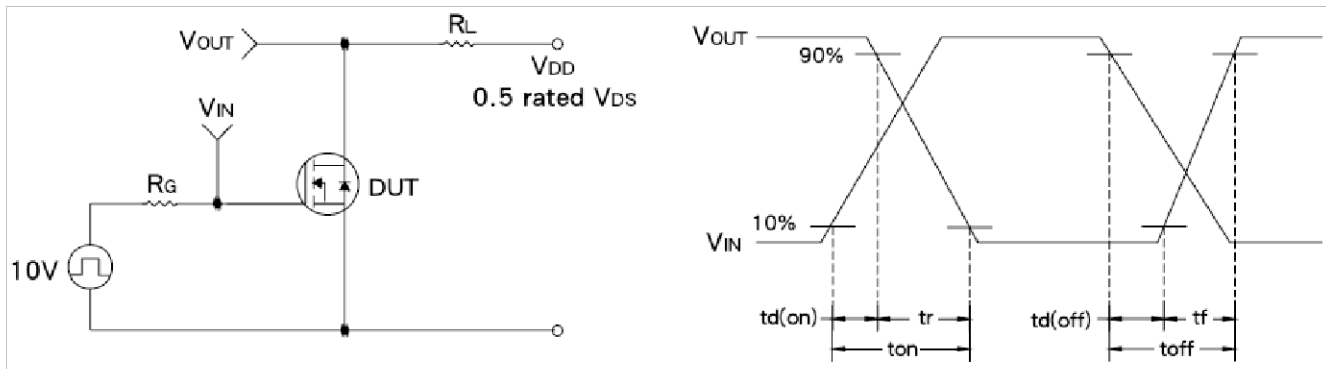
Note 4: Pulse test: pulse width ≤ 300μS, duty cycle ≤ 2%

Note 5: Essentially Independent of Operating Temperature

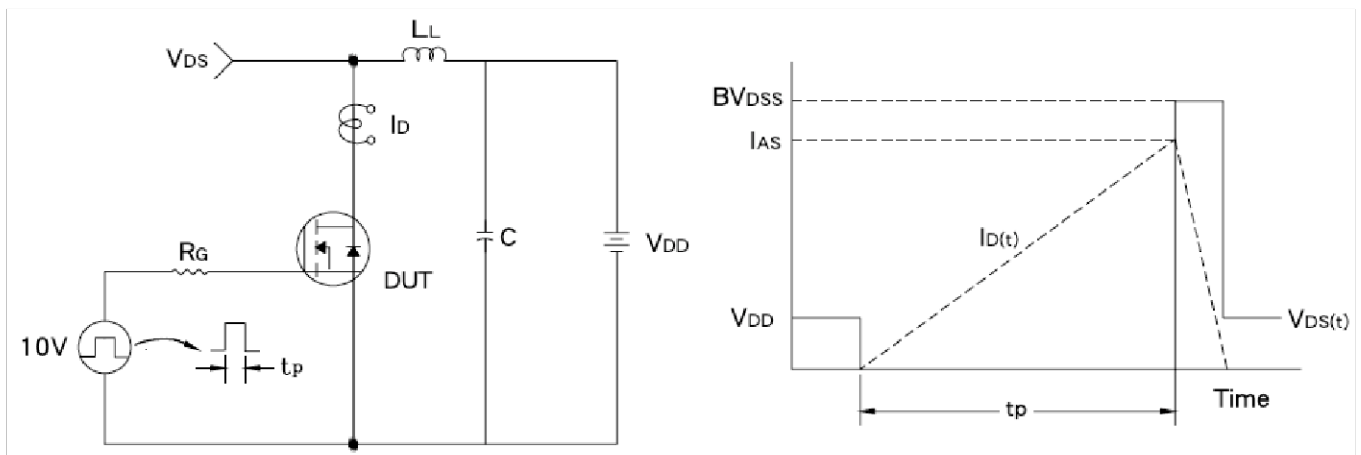
Gate Charge Test Circuit & Waveform



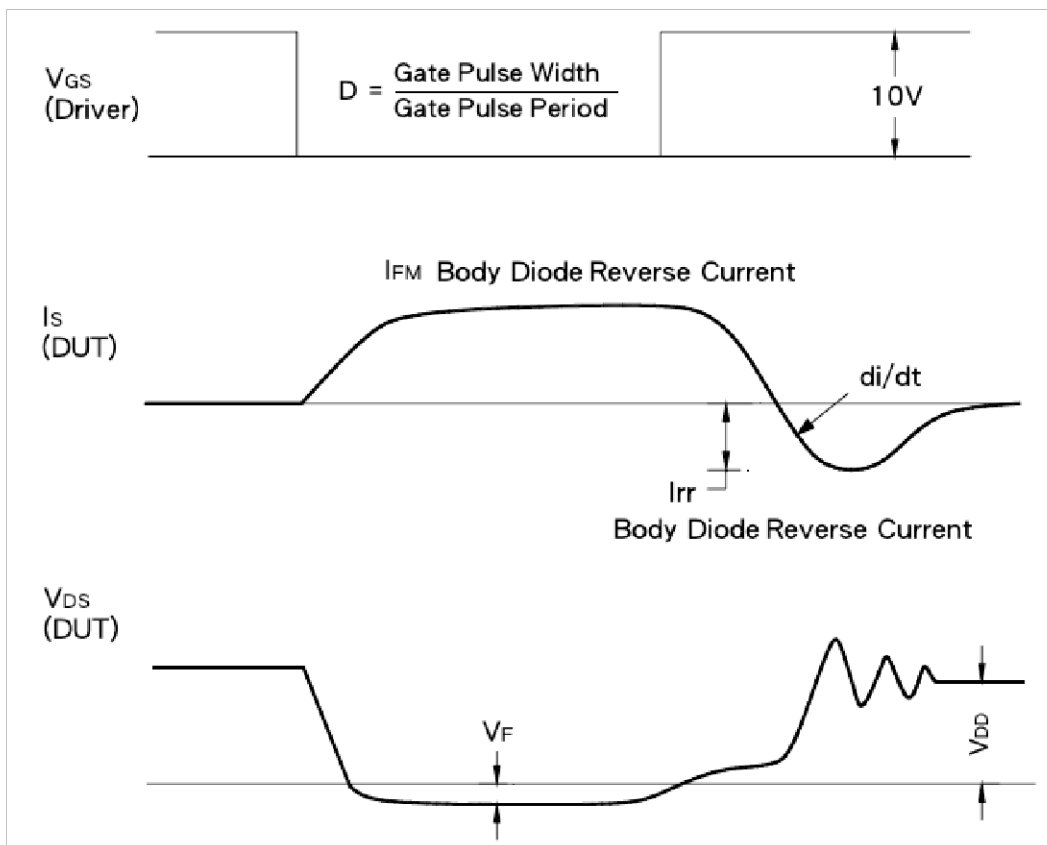
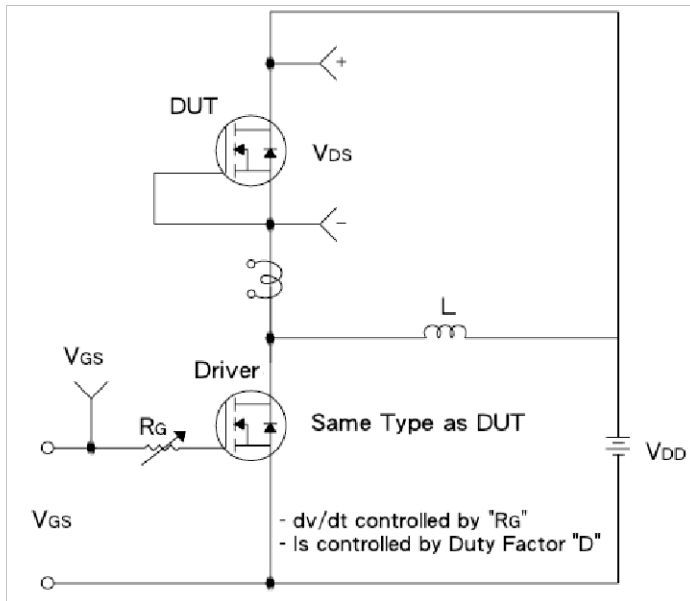
Resistive Switching Test Circuit & Waveform



E_{AS} Test Circuit & Waveform

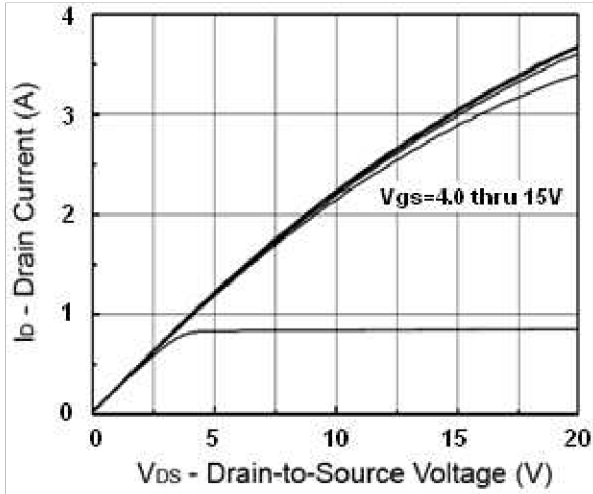


Diode Reverse Recovery Time Test Circuit & Waveform

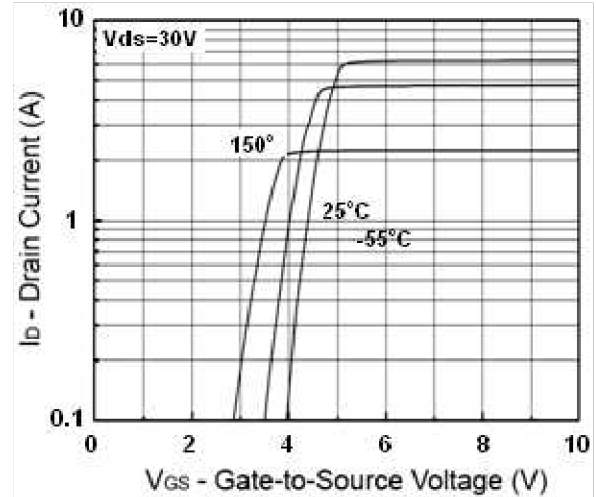


Electrical Characteristics Curve (Tc = 25°C, unless otherwise noted)

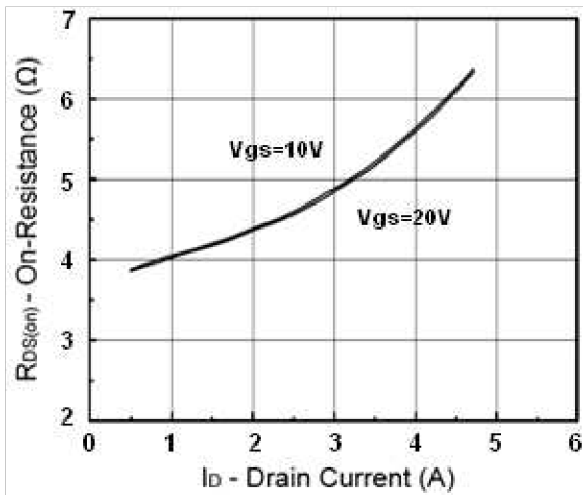
Output Characteristics



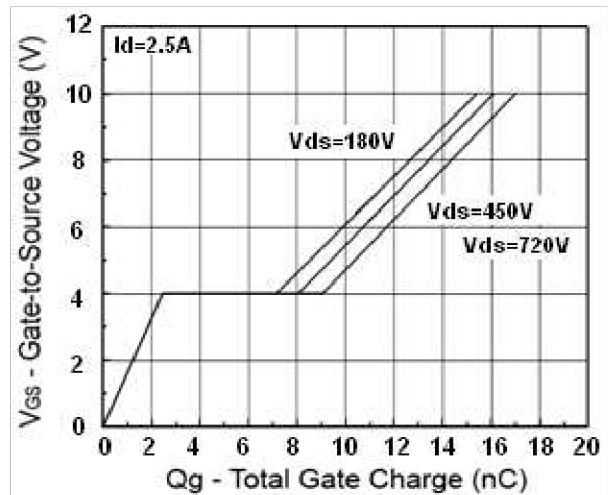
Transfer Characteristics



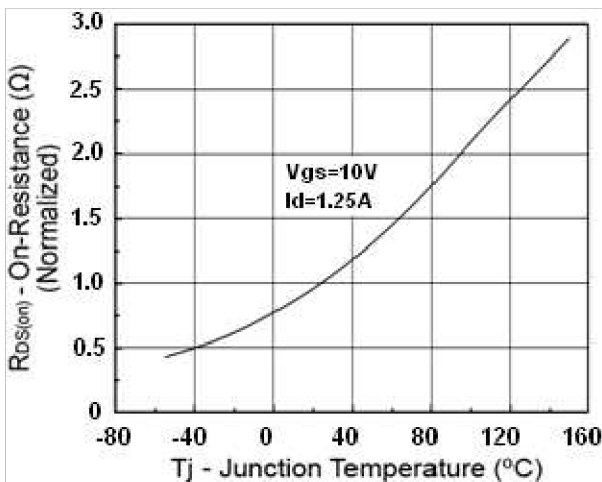
On-Resistance vs. Drain Current



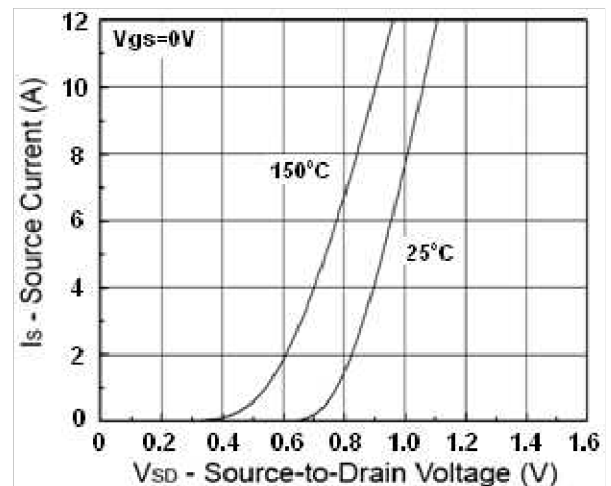
Gate Charge



On-Resistance vs. Junction Temperature

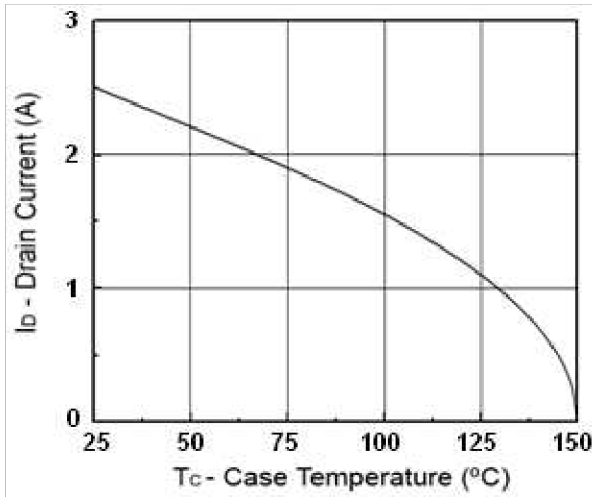


Source-Drain Diode Forward Voltage

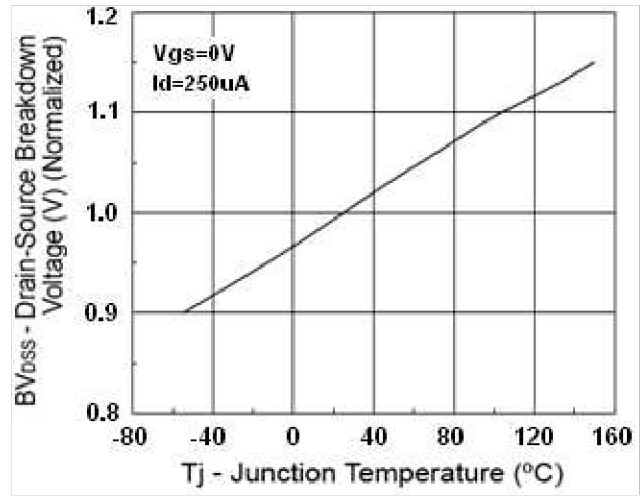


Electrical Characteristics Curve ($T_a = 25^\circ\text{C}$, unless otherwise noted)

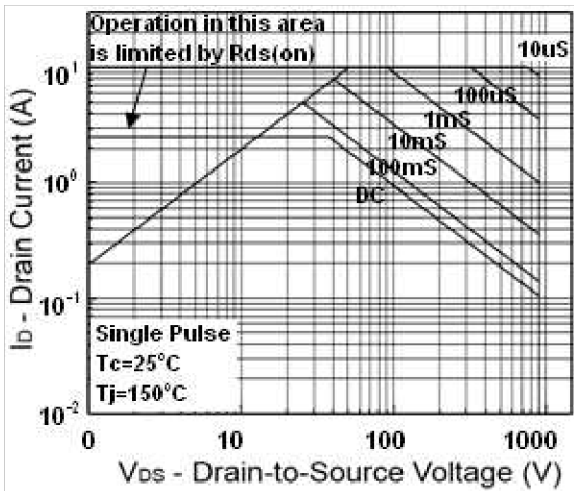
Drain Current vs. Case Temperature



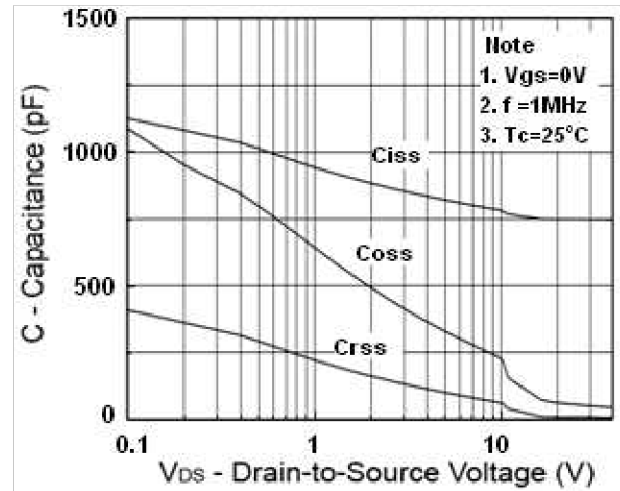
BV_{DSS} vs. Junction Temperature



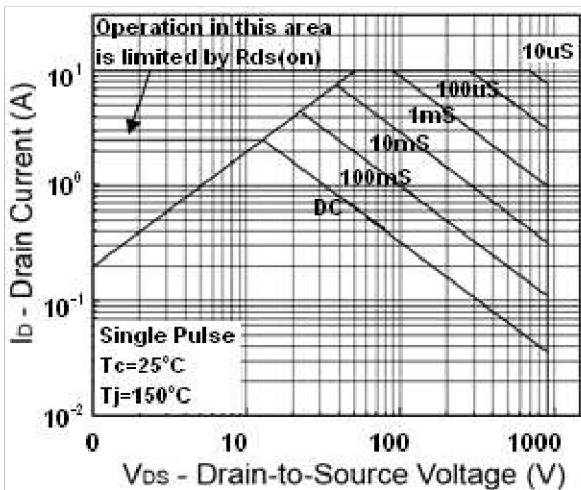
Maximum Safe Operating Area



Capacitance vs. Drain-Source Voltage

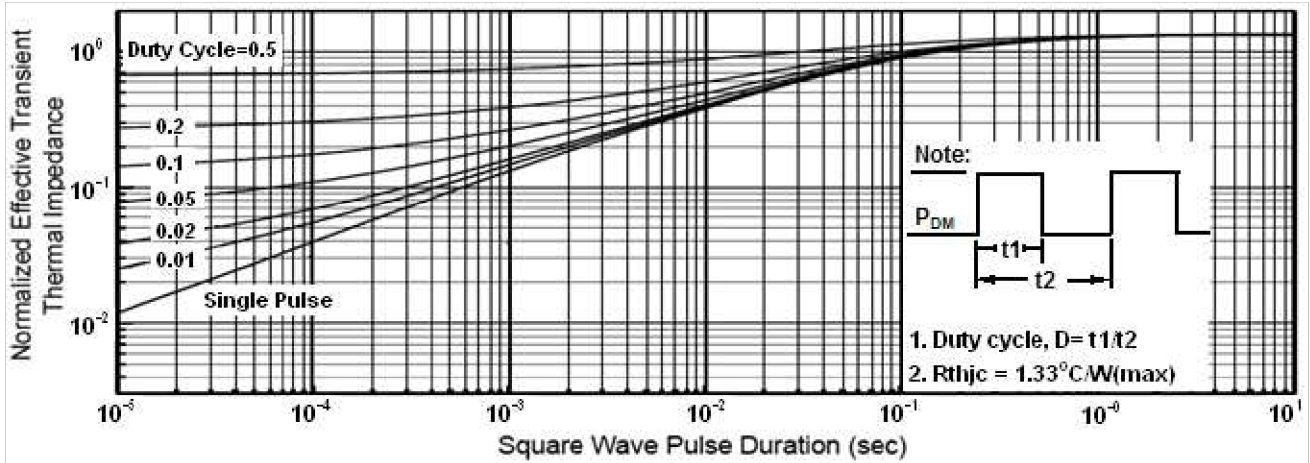


Maximum Safe Operating Area (ITO-220)

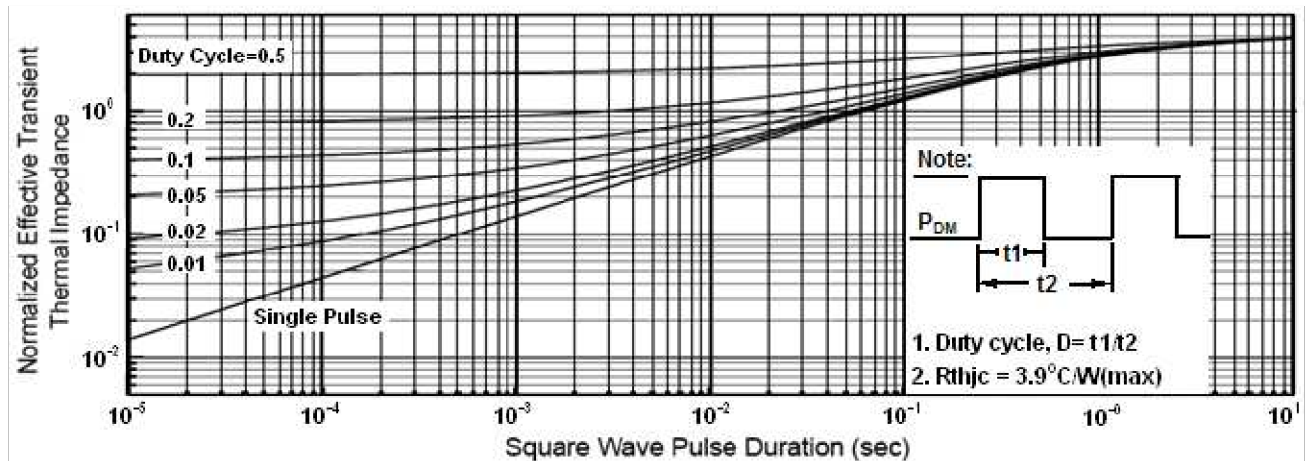


Electrical Characteristics Curve ($T_a = 25^{\circ}\text{C}$, unless otherwise noted)

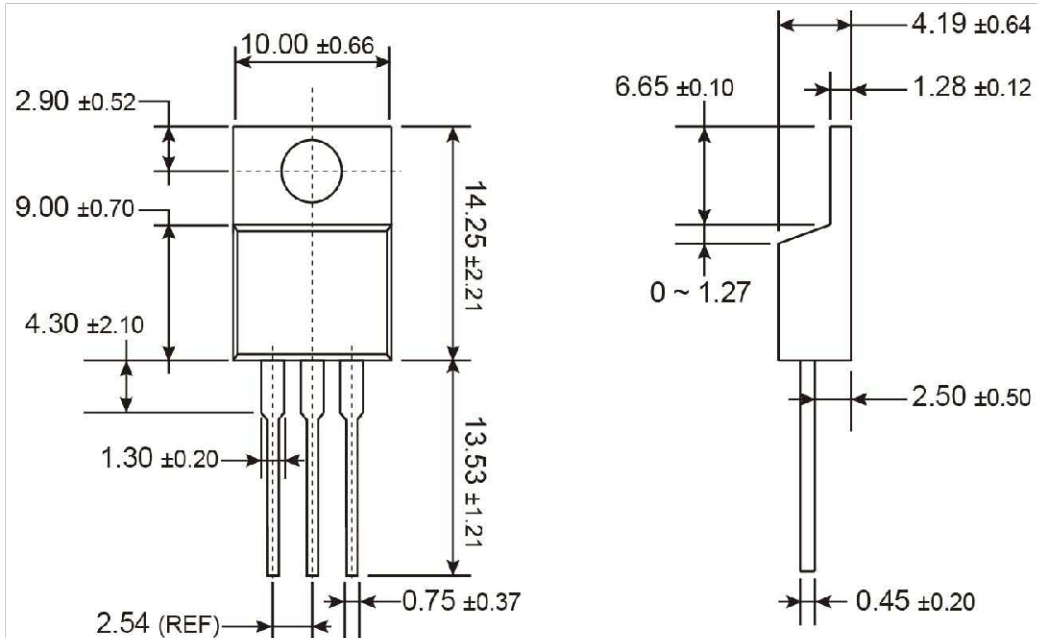
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient (ITO-220)

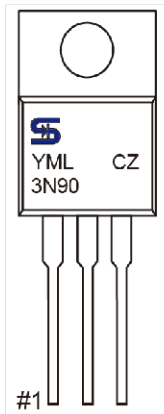


TO-220 Mechanical Drawing



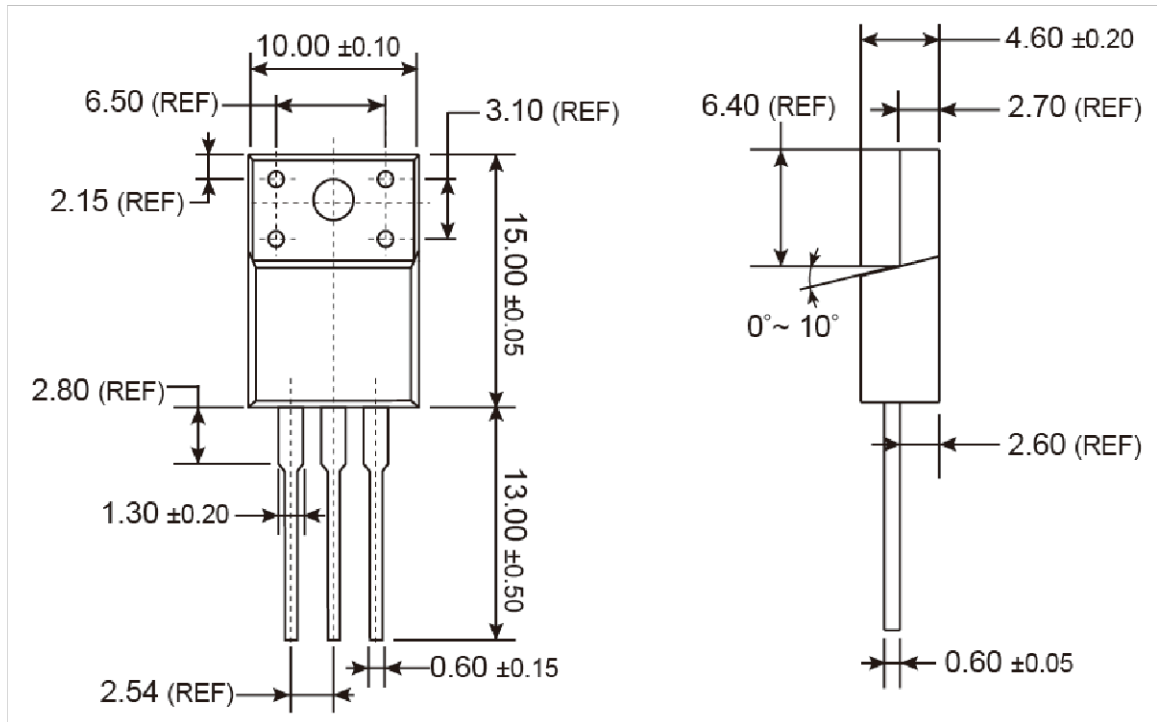
Unit: Millimeters

Marking Diagram



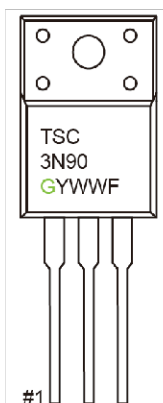
- Y** = Year Code
- M** = Month Code for Halogen Free Product
 - O** =Jan
 - P** =Feb
 - Q** =Mar
 - R** =Apr
 - S** =May
 - T** =Jun
 - U** =Jul
 - V** =Aug
 - W** =Sep
 - X** =Oct
 - Y** =Nov
 - Z** =Dec
- L** = Lot Code

ITO-220 Mechanical Drawing



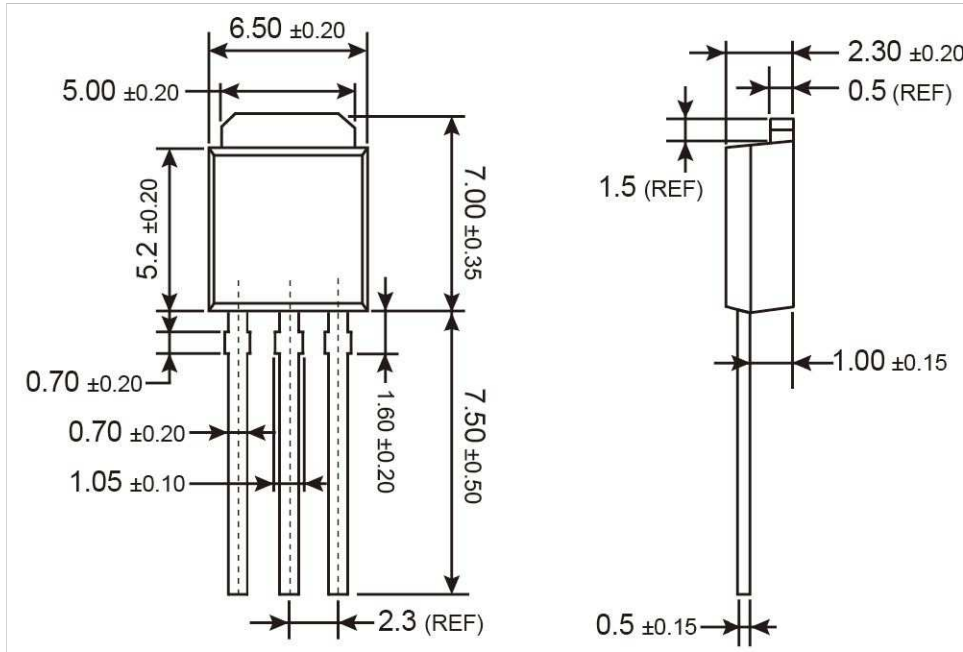
Unit: Millimeters

Marking Diagram



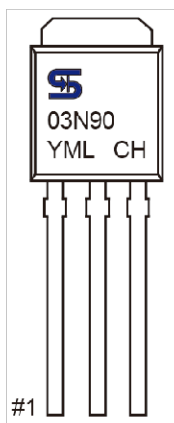
- G** = Halogen Free
- Y** = Year Code
- WW** = Week Code by Calendar Year
- F** = Factory Code

TO-251 Mechanical Drawing



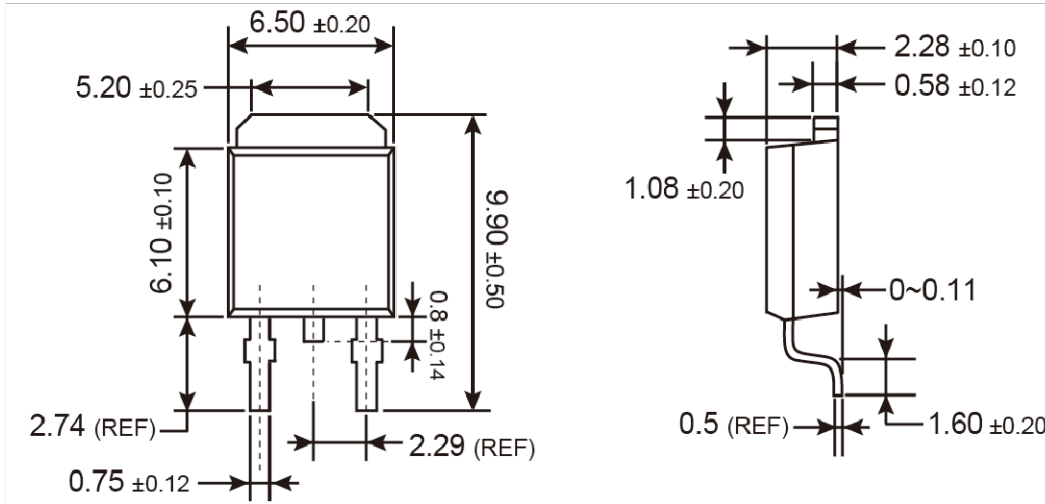
Unit: Millimeters

Marking Diagram



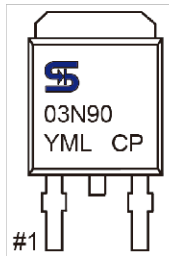
- Y** = Year Code
- M** = Month Code for Halogen Free Product
 - O** =Jan **P** =Feb **Q** =Mar **R** =Apr
 - S** =May **T** =Jun **U** =Jul **V** =Aug
 - W** =Sep **X** =Oct **Y** =Nov **Z** =Dec
- L** = Lot Code

TO-252 Mechanical Drawing



Unit: Millimeters

Marking Diagram



- Y** = Year Code
- M** = Month Code for Halogen Free Product
 - O** =Jan **P** =Feb **Q** =Mar **R** =Apr
 - S** =May **T** =Jun **U** =Jul **V** =Aug
 - W** =Sep **X** =Oct **Y** =Nov **Z** =Dec
- L** = Lot Code

Notice

Specifications of the products displayed herein are subject to change without notice. TSC or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, to any intellectual property rights is granted by this document. Except as provided in TSC's terms and conditions of sale for such products, TSC assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of TSC products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify TSC for any damages resulting from such improper use or sale.