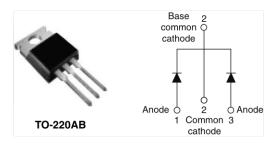


Vishay High Power Products

Schottky Rectifier, 16 A

IQR



PRODUCT SUMMARY				
I _{F(AV)} 16 A				
V _R 100 V				

FEATURES

- 175 °C T_J operation
- · Center tap configuration
- · Low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- · Designed and qualified for industrial level

DESCRIPTION

This center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	16	А	
V _{RRM}		100	V	
I _{FSM}	t _p = 5 μs sine	650	А	
V _F	8 Apk, T _J = 125 °C (per leg)	0.58	V	
T _J	Range	- 55 to 175	°C	

VOLTAGE RATINGS				
PARAMETER SYMBOL 16CTQ100GPbF UN				
Maximum DC reverse voltage	V _R	100	V	
Maximum working peak reverse voltage	V_{RWM}	100	V	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	L TEST CONDITIONS V		VALUES	UNITS
Maximum average forward current	per leg		I _{F(AV)} 50 % duty cycle at T _C = 148 °C, rectangular waveform		8	Α
See fig. 5	per device	'F(AV)			16	
Maximum peak one cycle no	· .		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	650	_
surge current per leg See fig. 7		I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	210	A
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 0.50 A, L = 60 mH		7.50	mJ
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5$ x V_R typical		0.50	Α	

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

Document Number: 94147 Revision: 04-Jun-07

16CTQ100GPbF

Vishay High Power Products Schottky Rectifier, 16 A



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	8 A	- T _J = 25 °C	0.72	- V
		16 A		0.88	
See fig. 1		8 A	T _J = 125 °C	0.58	
		16 A		0.69	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.28	mA
See fig. 2	'RM \''	T _J = 125 °C		7.0	
Threshold voltage	V _{F(TO)}	- T _J = T _J maximum		0.415	V
Forward slope resistance	r _t			11.07	mΩ
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		500	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 8.0		nH	
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation See fig. 4	3.25	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	-C/VV
Approximate weight				2	g
Approximate weight				0.07	oz.
Manusting to see a minimum			6 (5)	kgf · cm	
Mounting torque	maximum			12 (10)	(lbf · in)
Marking device				16CT	QG

Document Number: 94147 Revision: 04-Jun-07



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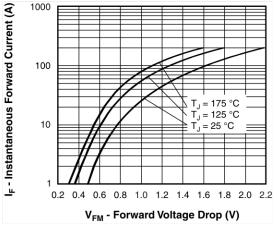


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

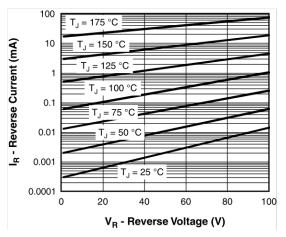


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

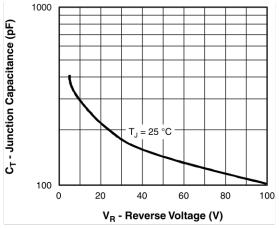


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

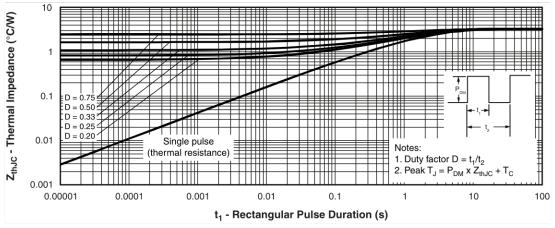


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

Vishay High Power Products Schottky Rectifier, 16 A



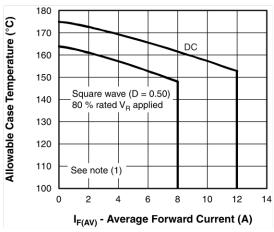


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

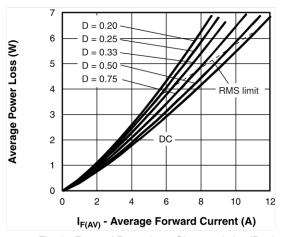


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

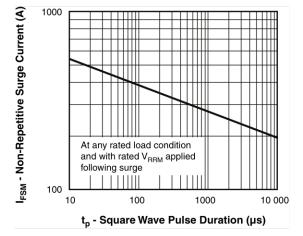


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

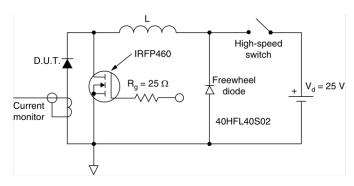


Fig. 8 - Unclamped Inductive Test Circuit

Note

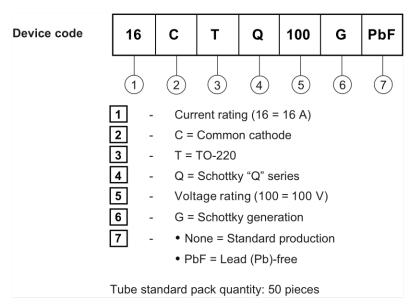
 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 10 V



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95222					
Part marking information http://www.vishay.com/doc?95215					

Document Number: 94147 Revision: 04-Jun-07



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Notice

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