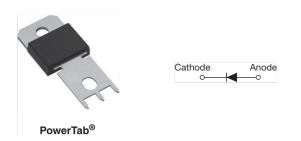


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High Performance Schottky Rectifier, 100 A



PRIMARY CHARACTERISTICS				
I _{F(AV)}	100 A			
V _R	100 V			
V _F at I _F	0.82 V			
I _{RM}	180 mA at 125 °C			
E _{AS}	9 mJ			
T _J max.	175 °C			
Package	PowerTab [®]			
Circuit configuration	Single			

FEATURES

- 175 °C max. operating junction temperature
- High frequency operation
- · Low forward voltage drop
- · Continuous high current operation
- Guard ring for enhanced ruggedness and long term reliability



- · Screw mounting only
- AEC-Q101 qualified
- PowerTab[®] package
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-100BGQ100HF4 Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
,	Rectangular waveform	100	А	
I _{F(AV)}	T _C	124	°C	
V _{RRM}		100	V	
I _{FSM}	t _p = 5 µs sine	6300	А	
V _F	100 A _{pk} (typical)	0.77	V	
	T _J	125	°C	
TJ	Range	-55 to +175	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-100BGQ100HF4	UNITS	
Maximum DC reverse voltage	V _R	100		
Maximum working peak reverse voltage	V _{RWM}			

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_C = 124 °C, rectangular waveform 100 A		Α	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	6300	A
non-repetitive surge current	IFSM	10 ms sine or 6 ms rect. pulse		800	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 4.5 mH 9 mJ		mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \text{ x } V_R$ typical		А	



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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES		UNITS
FARAWETER	STWIBOL	1231 00	TEST CONDITIONS		MAX.	UNITS
	V _{FM} ⁽¹⁾	50 A	T _J = 25 °C	0.83	0.86	V
Forward voltage drop		100 A		1.01	1.08	
		50 A	T _J = 125 °C	0.66	0.7	
		100 A		0.77	0.82	
Reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	22	300	μΑ
neverse leakage current	'RM \''	T _J = 125 °C		14	18	mA
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz) 25 °C		13	20	pF
Typical series inductance	L _S	Measured from tab to mounting plane		3	.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs		

Note

 $^{^{(1)}}$ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction at temperature range	nd storage	T _J , T _{Stg}		-55 to +175	°C	
Maximum thermal re junction to case	sistance,	R _{thJC}	DC operation	0.50	°C/W	
Typical thermal resis case to heatsink	tance,	R _{thCS}	Mounting surface, smooth and greased	0.30	3C/VV	
Approximate weight				5	g	
Approximate weight				0.18	oz.	
Mounting torque	minimum			1.2 (10)	N⋅m	
	maximum			2.4 (20)	(lbf·in)	
Marking device			Case style PowerTab®	100BGQ100H		

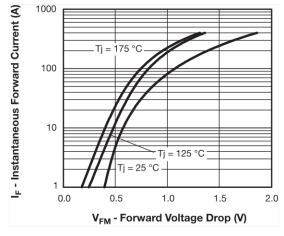


Fig. 1 - Maximum Forward Voltage Drop Characteristics

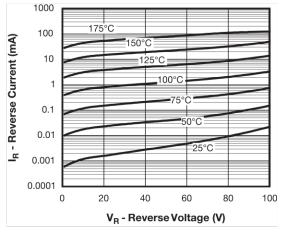


Fig. 2 - Typical Values of Reverse Current vs.
Reverse Voltage



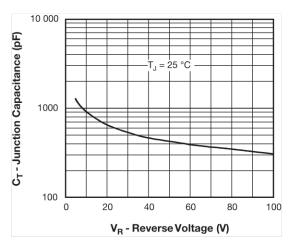


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

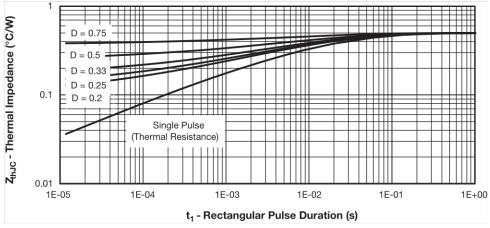


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

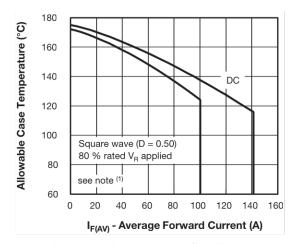


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

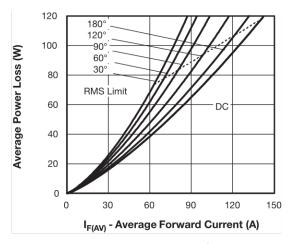


Fig. 6 - Forward Power Loss Characteristics

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

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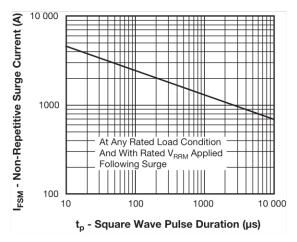


Fig. 7 - Maximum Non-Repetitive Surge Current

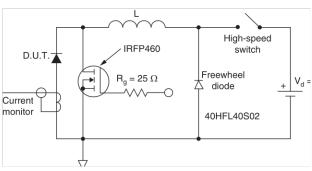
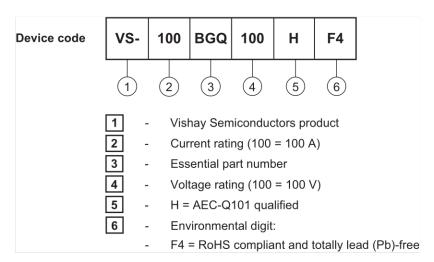


Fig. 8 - Unclamped Inductive Test Circuit

ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-100BGQ100HF4	25	375	Antistatic plastic tube	

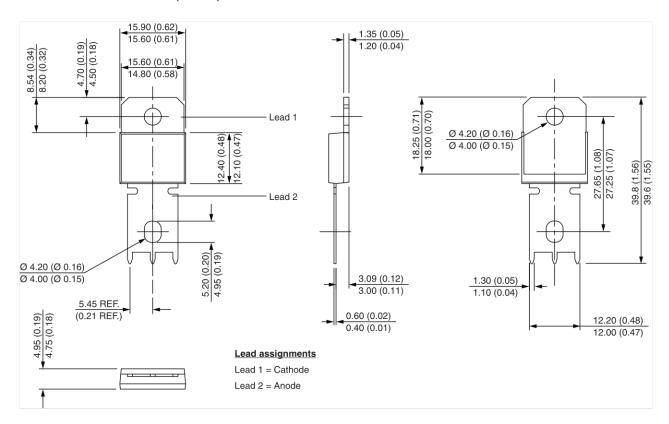
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95240</u>				
Part marking information	www.vishay.com/doc?95467			
Application note	www.vishay.com/doc?95179			
SPICE model	www.vishay.com/doc?96588			



Vishay Semiconductors

PowerTab[®]

DIMENSIONS in millimeters (inches)





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